EXHIBIT A

	An assignment from the	inventor(s) of U.S. Patent Application Serial No. <u>D9/92/</u> 639
filed ₋	March 8, 2000	has been recorded in the U.S. Patent and Trademark Office at
Reel_	010070 , Frame 03	AD.

EXHIBIT B Patents and Patent Applications Subject to Revocations and Substitute Power of Attorney

		18-Mar-02	10/101,260	COMPACT LASER PACKAGE WITH INTEGRATED TEMPERATURE CONTROL	9775-063-999	15436.253.39.1
		18-Mar-02	10/101,247	OPTOELECTRONIC TRANSCEIVER MODULE WITH THERMALLY ISOLATED COMPONENTS	9775-059-999	15436.253.37.1
		6-Aug-01	09/923,471	SYSTEM AND METHOD FOR PACKAGING A LASER/DETECTOR	9775-057-999	15436.253.35.1
		8-Aug-01	09/925,176	SIGNAL PROCESSING CIRCUIT FOR FLOATING SIGNAL SOURCES USING POSITIVE FEEDBACK	9775-055-999	15436.253.33
		5-Feb-01	09/777,917	INTEGRATED MEMORY MAPPED CONTROLLER CIRCUIT FOR FIBER OPTICS TRANSCEIVERS	9775-052-999	15436.253.32
		31-Aug-01	09/929,737	MULTI-RATE AND MULTI-LEVEL GIGABIT INTERFACE CONVERTER	9775-051-999	15436.253.31
		4-Dec-01	10/005,924	CIRCUIT INTERCONNECT FOR OPTOELECTRONIC DEVICE FOR CONTROLLED IMPEDANCE AT HIGH FREQUENCIES	9775-048-999	15436.253.29
		17-Oct-00	09/691,311	FIBER OPTIC HEADSET FOR WIRELESS TELEPHONES	9775-047-999	15436.253.28
		14-Nov-01	10/003,959	OPTOELECTRONIC DEVICE CAPABLE OF PARTICIPATING IN IN-BAND TRAFFIC	9775-043-999	15436.253.26.1
		12-Oct-01	09/976,765	A SYNCHRONOUS NETWORK TRAFFIC PROCESSOR	9775-042-999	15436.253.25.1
		12-Dec-00	09/735,710	SYSTEM AND METHOD FOR TRANSMITTING DATA ON RETURN PATH OF A CABLE TELEVISION SYSTEM	9775-040-999	15436.24.1
		19-Sep-01	09/957,557	COMPACT OPTICAL ASSEMBLY FOR OPTOELECTRONIC TRANSCEIVERS	9775-039-999	15436.253.23
		31-Oct-02	10/285,106	SIGNAL STRENGTH DETECTION IN HIGH-SPEED OPTICAL ELECTRONICS	9775-036-999	15436.253.21
		31-Oct-02	10/285,083	SIGNAL STRENGTH DETECTION IN HIGH SPEED OPTICAL ELECTRONICS	9775-034-999	15436.253.19.1
		8-Mar-00	09/521,639	FIBER OPTIC LASER TRANSMITTER WITH REDUCED NEAR END REFLECTIONS	9775-031-999	15436.253.18
		19-Oct-99	09/420,947	A TRANSCEIVER WITH AUXILIARY MONITORING PORTS	9775-030-999	15436.253.17
19-Sep-00	6121838	23-Dec-98	09/221,673	A PRECISION GAAS LOW-VOLTAGE DC AMPLIFIER	9775-026-999	15436.253.16
15-Oct-96	5566171	15-Mar-95	08/404,873	HIGH SPEED MESH CONNECTED LOCAL AREA	9775-022-999	15436.253.14
28-May-91	5019769	14-Sep-90	07/583,178	SEMICONDUCTOR LAS DIODE CONTROLLER AND LASER DIODE BIASING CONTROL METHOD	9775-021-999	15436.253.13
4-Apr-95	5404505	1-Nov-91	07/786,453	HIGH SPEED INFORMATION BROADCASTING SYSTEM	9775-020-999	15436.253.12
22-May-90	4927225	30-May-89	07/358,892	2X2 OPTICAL BYPASS SWITCH	9775-017-999	15436.253.9
21-Nov-89	4881789	26-May-88	07/189,979	INTEGRATED OPTICAL COUPLER AND CONNECTOR	9775-016-999	15436.253.8
18-Feb-97	5604735	12-May-95	08/440.088	HIGH SPEED NETWORK SWITCH	9775-015-999	15436.253.7
21-Sep-99	5956168	5-Sep-97	08/924,852	MULTI-PROTOCOL DUAL FIBER LINK LASER DIODE	9775-007-999	15436.253.2
31-Jul-01	6268808	8-Jun-99	09/327,997	HIGH SPEED MODIFICATION SYSTEM AND METHOD	9775-005-999	15436.253.1
issue Date	- Patenti#1	Filing)Date	Serial #∵	ent	Figuros (4)	#Well-lib

EXHIBIT B Patents and Patent Applications Subject to Revocations and Substitute Power of Attorney

		31-Oct-02	10/285,081	A SYSTEM AND METHOD OF DETECTING A BIT PROCESSING ERROR	9775-096-999	15436.253.58.1
		31-Oct-02	10/285,082	A SYSTEM AND METHOD OF PROCESSING DATA SIGNAL	9775-095-999	15436.253.57
		19-Mar-03	10/393,217	CIRCUIT BOARD HAVING TRACES WITH DISTINCT TRANSMISSION IMPEDANCES	9775-094-999	15436.253.56.1
		19-Mar-03	10/393,164	A TRANSMISSION LINE WITH INTEGRATED CONNECTION PADS	9775-093-999	15436.253.55.1
		30-Oct-02	10/285,204	TRANSIMPEDANCE AMPLIFIER ASSEMBLY WITH SEPARATE GROUND LEADS AND SEPARATE POWER LEADS FOR INCLUDED CIRCUITS	9775-092-999	15436.253.54.1
		19-Mar-03	10/393,218	A SUBMOUNT, PEDESTAL, AND WIRE BOND ASSEMBLY FOR A TRANSISTOR OUTLINE PACKAGE WITH REDUCED WIRE BOND INDUCTANCE	9775-091-999	15436.253.53.1
		19-Mar-03	10/393,215	TRANSISTORS OUTLINE PACKAGE WITH EXTERIORLY MOUNTED RESISTERS	9775-090-999	15436.253.52.1
		31-Oct-02	10/285,105	METHOD FOR MAINTAINING DESIRABLE OPTICAL PERFORMANCE OF LASER EMITTERS OVER TEMPERATURE VARIATIONS	9775-088-999	15436.253.51.1
		21-Jan-03	10/348,341	EXTENDED BANDWIDTH SEMICONDUCTOR OPTICAL AMPLIFIERS	9775-087-999	15436.253.50.1
		23-Jan-03	10/351,620	TRANSMITTER OPTICAL SUBASSEMBLY WITH VOLUME PHASE HOLOGRAPHIC OPTICS	9775-086-999	15436.253.49.1
		2-Jul-02	10/188,575	SYSTEM FOR CONTROLLING BIAS CURRENT IN LASER DIODES WITH IMPROVED SWITCHING RATES	9775-085-999	15436.253.48
		19-Mar-02	10/102,625	EFFICIENT TRANSMISSION OF DIGITAL RETURN PATH DATA IN CABLE TELEVISION RETURN PATH	9775-078-999	15436.253.47.1
		30-Oct-02	10/285,205	CABLE TELEVISION RETURN LINK SYSTEM WITH DATA-RATE SIDE-BAND COMMUNICATION CHANNELS	9775-075-999	15436.253.46.1
		22-Oct-01	10/036,995	MULTIPLE WIDTH TRANSCEIVER HOST BOARD SYSTEM	9775-074-999	15436.253.45
		18-Mar-02	10/101,258	AVALANCHE PHOTODIODE CONTROLLER CIRCUIT FOR FIBER OPTICS TRANSCEIVER	9775-073-999	15436.253.44.1
		3-Feb-03	10/357,918	APPARATUS AND METHOD FOR COMBINING ASYNCHRONOUS DIGITAL SIGNALS IN CABLE TELEVISION RETURN PATH	9775-072-999	15436.253.43.1
	2.	3/19/2002	10/102,619	DATA RATE COMPRESSION DEVICE FOR CABLE TELEVISION RETURN PATH USING BANDPASS PUNCTURING	9775-071-999	15436.253.42.1
		12-Aug-02	10/218,344	BANDPASS COMPONENT DECIMATION AND TRANSMISSION OF DATA IN CABLE TELEVISION DIGITAL RETURN PATH	9775-070-999	15436.253.41.1
		18-Mar-02	10/101,248	CONTROL CIRCUIT FOR OPTOELETRONIC MODULE WITH INTEGRATED TEMPERATURE CONTROL	9775-065-999	15436.253.40.1
#Issue Date	Patent#	Filing Date	Serial#	eni.	Previous Law	#JOIN SWW

EXHIBIT B
Patents and Patent Applications Subject to Revocations and Substitute Power of Attorney

		17-Apr-03	10/419,023	METHOD AND APPARATUS FOR REDUCING INTERFERENCE IN AN OPTICAL DATA STREAM USING DATA-INDEPENDENT EQUALIZATION	9775-152-999	15436.253.81
		8-Nov-02	10/291,208	9 EFFICIENT TRANSMISSION OF DIGITAL RETURN PATH DATA IN CABLE TELEVISION RETURN PATH	9775-145-999	15436.253.79
		31-Oct-02	10/285,369	9775-137-999 MAINTAINING DESIRABLE PERFORMANCE OF OPTICAL EMITTERS AT EXTREME TEMPERATURS	9775-137-999	15436.253.76
		30-Oct-02	10/285,203	9 SYSTEM FOR CONTROLLING BIAS CURRENT IN LASER DIODES WITH IMPROVED SWITCHING RATES	9775-130-999	15436.253.72.1
		7-Mar-03	10/384,227	9 STAGED AMPLIFIER FOR LOWER NOISE FIGURE AND HIGHER SATURATION POWER	9775-128-999	15436.253.70.1
		7-Mar-03	10/384,228	9 DUAL FIBER OPTIC AMPLIFIER WITH SHARED PUMP SOURCE	9775-127-999	15436.253.69.1
		16-May-02	10/147,677	9 SIGNAL PROCESSING CIRCUIT FOR FLOATING SIGNAL SOURCES USING POSITIVE FEEDBACK	9775-109-999	15436.253.64
		18-Sep-02	10/246,038		8775-107-999	15436.253.63
		8-Oct-02	10/266,870		9775-105-999	15436.253.62
		8-Oct-02	10/266,869	9 SYSTEM AND METHOD FOR PROTECTING EYE SAFETY DURING OPERATION OF A FIBER OPTIC TRANSCEIVER	9775-103-999	15436.253.83
		5-Nov-02	10/288,324	•	9775-101-999	15436.253.60
		1-Nov-02	10/285,772	9 APPARATUS FOR ENHANCING EMPEDANCE- MATCHING IN A HIGH-SPEED DATA COMMUNICATIONS SYSTEM	9775-098-999	15436.253.59.1
issue Date	Patent#	Filing Date	Serial#*	Titlo	Pievieus Law Film Robe	WNSIFIID#